

Low Cost 8051 μ C Starter Kit/ Development Board HT-ISP900

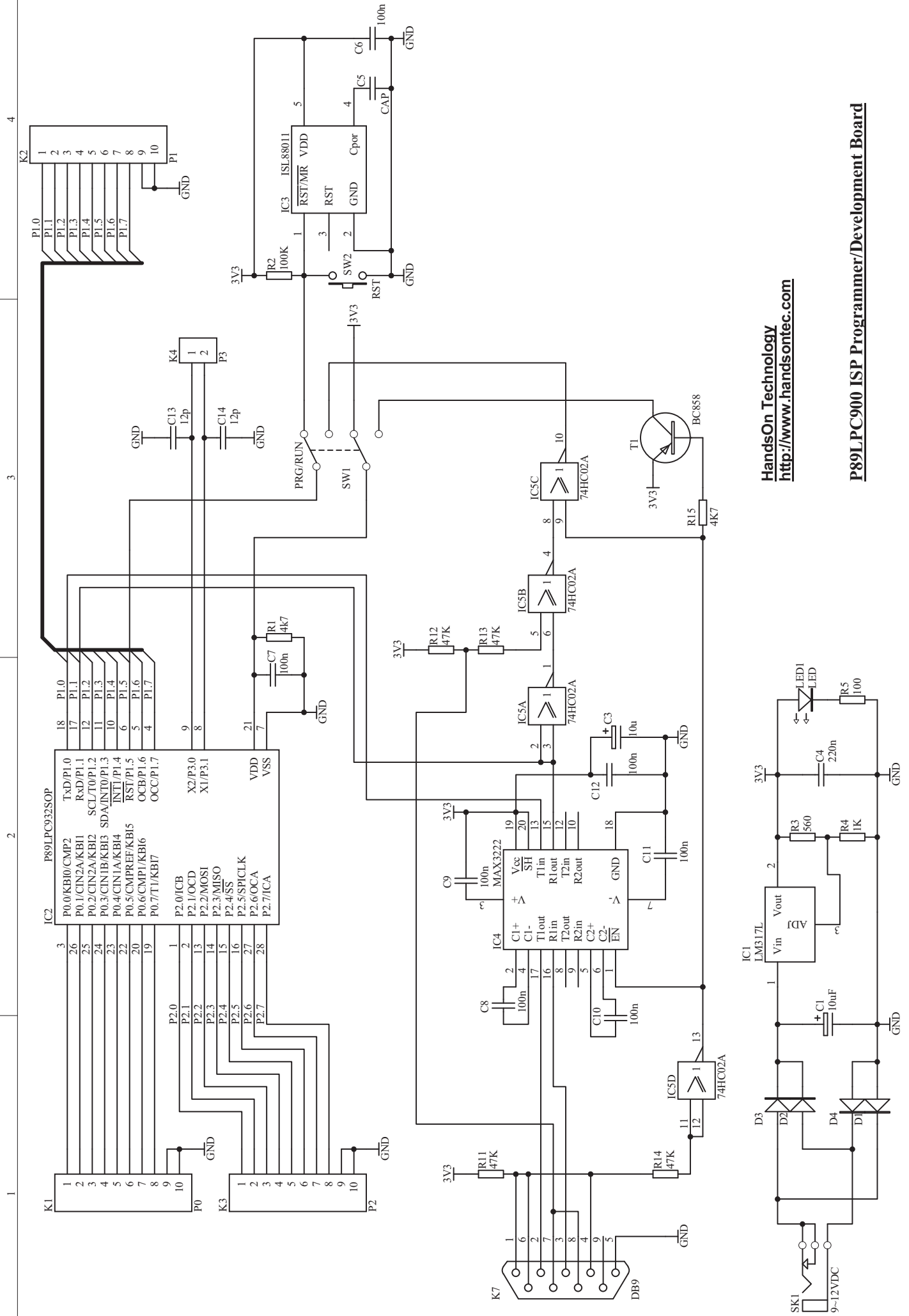
HT-ISP900 is an ideal platform for small to medium scale embedded systems development and quick 8051 embedded design prototyping. **HT-ISP900** can be used as stand-alone 8051 μ C Flash programmer or as a development, prototyping and educational platform



Main Features:

- 8051 Central Processing Unit with 2X cycles.
- On-chip Flash Program Memory with In-System Programming (ISP) and In Application Programming (IAP) capability.
- Boot ROM contains low level Flash programming routines for downloading code via the RS232.
- Flash memory reliably stores program code even after 10,000 erase and program cycles.
- 16KB of Flash ROM, 768B of RAM and 512B of EEPROM.
- 10-year minimum data retention.
- Programmable security for the code in the Flash. The security feature protects against software piracy and prevents the contents of the Flash from being read.
- 4 level priority interrupt & 7 interrupt sources.
- 26 general purpose I/O pins connected to 10pins header connectors for easy I/O pins access.
- Full-duplex enhanced UART – Framing error detection Automatic address recognition.
- Programmable Counter Array (PCA) & Pulse Width Modulation (PWM).
- Three 16-bits timer/event counters and Real-Time-Clock.
- AC/DC (9~12V) power supply – easily available from wall socket power adapter.
- Industrial popular window **Keil** C compiler and assembler included (Eval. version).
- Free **Flash Magic** Windows software for easy program code down loading.

PLEASE READ **HT-ISP900 GETTING STARTED MANUAL** BEFORE OPERATE THIS BOARD
INSTALL ACROBAT READER (AcrobatReader705 Application) TO OPEN AND PRINT ALL DOCUMENTS



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P89LPC900 ISP Programmer/Development Board